Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5580-00600 APPLICANT: Rowlands et al. FILING DATE: October 6, 2000	SERIAL NO. 09/680,524 GROUP: 2181

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

ᴋᴋ	A1	Halfhill, "SiByte Reveals 64-Bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48.
ᴋᴋ	A2	Pentium® Pro Family Developer's Manual, Vol. 1: Specifications, Chapter 4, pp. 1-18, 1996.

EXAMINER: _Kim T. Huynh_   DATE CONSIDERED: _3/06/03_

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

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uh	B8	6,157,623	12/5/00	Kerstein			
uh	B9	6,262,594	7/17/01	Cheung, et al.			

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uh	B1	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm, January 15, 2001, 2 pages.
uh	B2	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm, January 15, 2001, 3 pages.
uh	B3	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian, January 15, 2001, 2 pages.
hh	B4	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
uh	B5	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.
uh	B6	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
uh	B7	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.

EXAMINER: _Kim Huynh_   DATE CONSIDERED: _3/06/03_

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Form PTO-1449 (modified) IPE List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary).	ATTY. DKT. NO. 5580-00600 APPLICANT: Rowlands et al. FILING DATE: October 6, 2000	SERIAL NO. 09/680,524 GROUP: 2181

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kh	C1	5,778,414	7/7/98	Winter, et al.			

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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
kh	C2	00/30322	5/25/00	WO			
kh	C3	00/52879	9/8/00	WO			

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kh	C4	"ATLAS I: A Single-Chip, Gigabit ATM Switch with HIC/HS Links and Multi-Lane Back-Pressure," Katevenis, et al., IPC Business Press LTD., Long, GB, vol. 21, no. 7-8, March 30, 1998, XP004123981, 5 pages.
kh	C5	"An Introductory VHDL Tutorial: Chapter 1 - An Introduction and Background," 1995, Green Mountain Computing Systems, XP002212233, 2 pages.

EXAMINER: Kim T Huynh   DATE CONSIDERED: 3/6/13

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